

variably controlling one of the input and second signals; and  
mixing the variably controlled signal and the not variably controlled signal to produce  
variably controllable third, fourth and fifth channel output signals.

[ Please add new claim 3 as follows: ]

3. A circuit for processing at least one channel input signal comprising:  
means for receiving the input signal;  
means for modifying said received signal to produce a second signal;  
means for variably controlling at least one of said input and second signals; and  
means for mixing said variably controlled signal and the not variably controlled  
signal to produce variably controllable third, fourth and fifth channel output signals.

[ Please add new Claim 4 as follows: ]

*al*  
*cont* 4. A method of processing at least one channel input signal comprising the  
steps of:

receiving the input signal;  
modifying the input signal to produce a second signal;  
variably controlling the input and second signals; and  
mixing the variably controlled signals produce variably controllable third, fourth and  
fifth channel output signals.

[ Please add new Claim 5 as follows: ]

5. A circuit for processing at least one channel input signal comprising:  
means for receiving the input signal;  
means for modifying said received signal to produce a second signal;  
means for variably controlling said input and second signals; and